

HPC Challenge Benchmark Suite

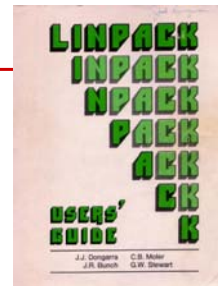
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 Piotr Luszczek (ICL/UTK)

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Accidental Benchmark

- First benchmark report from 1977;
 - Cray 1 to DEC PDP-10
- Appendix B of the Linpack Users' Guide
 - Designed to help users extrapolate execution time for Linpack software package



2×10^3
 2×10^3
 2×10^3

UNIT = 10**6 TIME/(1/3 100**3 + 100**2)

Facility	TIME N=100 micro- secs.	UNIT micro- secs.	Computer	Type	Compiler
NCAR	14.0	.049	0.14	CRAY-1	S CFT, Assembly-62AS
LASL	4.64	.148	0.43	CDC 7600	S FIN Assembly BLAS
NCAR	3.57	.192	0.36	CRAY-1	S CFT
LASL	5.27	.210	0.61	CDC 7600	S PTN
Argonne	2.31	.297	0.86	IBM 370/195	D H
NCAR	1.81	.359	1.05	CDC 7600	S Local
Argonne	1.77	.388	1.33	IBM 3033	D H
NASA Langley	1.49	.489	1.42	CDC Cyber 175	S PTN
U. Ill. Urbana	1.54	.506	1.47	CDC Cyber 175	S Ext. 4.6
ILL	1.34	.554	1.61	CDC 7600	S CHAT, No optimize
SLAC	1.19	.579	1.69	IBM 370/168	D H Ext., Fast mult.
Michigan	1.09	.631	1.84	Aradahl 470/V6	D H
Toronto	.77	.690	2.39	IBM 370/165	D H Ext., Fast mult.
Northwestern	.47	1.44	4.20	CDC 6600	S PTN
Texas	.54	1.93	5.63	CDC 6600	S RUN
China Lake	.75	1.95	5.69	Univac 1110	S V
Yale	.36	2.59	7.53	DEC KL-20	S F20
Bell Labs	.17	3.46	10.1	Honeywell 6080	S Y
Wisconsin	.17	3.49	10.1	Univac 1110	S V
Iowa State	.19	3.54	10.2	Intel AS/5 mod3	D H
U. Ill. Chicago	.14	4.10	11.9	IBM 370/158	D G1
Purdue	.17	5.69	16.6	CDC 6500	S FUN
U. C. San Diego	.13	13.1	38.2	Burroughs 6700	S H
Yale	.17	17.1	49.9	DEC KA-10	S F40

* TIME(100) = (100/75)**3 SGEFA(75) + (100/75)**2 SGEFL(75)

My kids
Playstation 2
unoptimized.

My PDA running
the benchmark in
Java.

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Linpack Benchmark Over Time

- ♦ In the beginning there was the Linpack 100 Benchmark (1977)
 - n=100 (80KB); size that would fit in all the machines
 - Fortran; 64 bit floating point arithmetic
 - No hand optimization (only compiler options)
- ♦ Linpack 1000 (1986)
 - n=1000 (8MB); wanted to see higher performance levels
 - Any language; 64 bit floating point arithmetic
 - Hand optimization OK
- ♦ Linpack TPP (1991) (Top500; 1993)
 - Any size (n as large as you can; n=1.2M; 11.5TB; ~3 hours);
 - Any language; 64 bit floating point arithmetic
 - Hand optimization OK
 - Strassen's method not allowed (confuses the op count)
 - Reference implementation available
- ♦ In all cases results are verified by looking at: $\frac{\|Ax-b\|}{\|A\| \|x\| n \varepsilon} = O(1)$
- ♦ Operations count for factorization $\frac{2}{3}n^3 - \frac{1}{2}n^2$; solve $2n^2$

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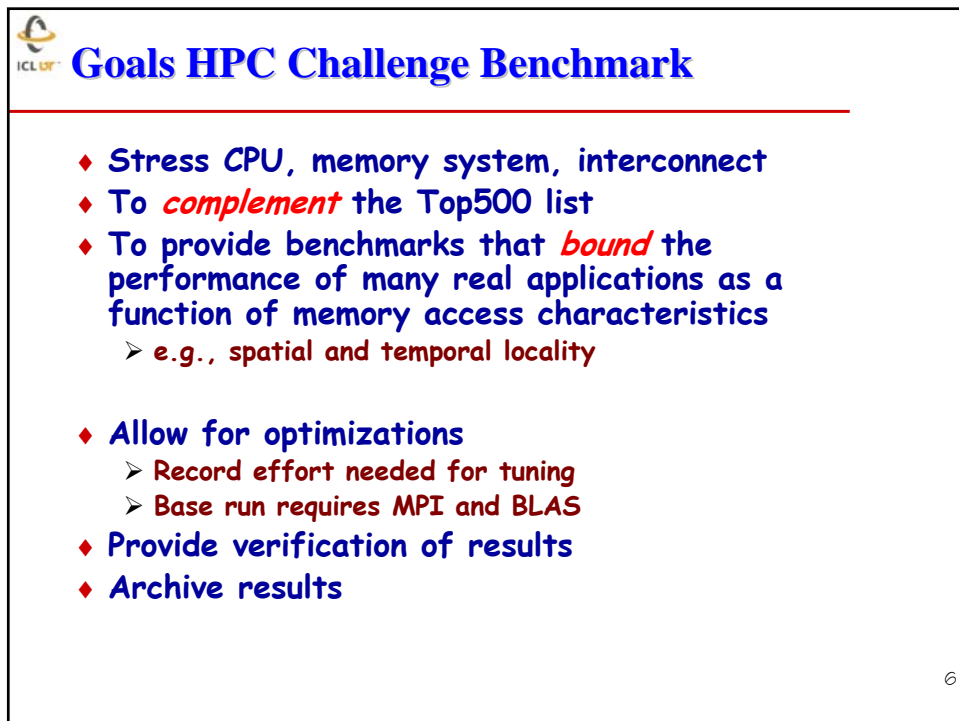
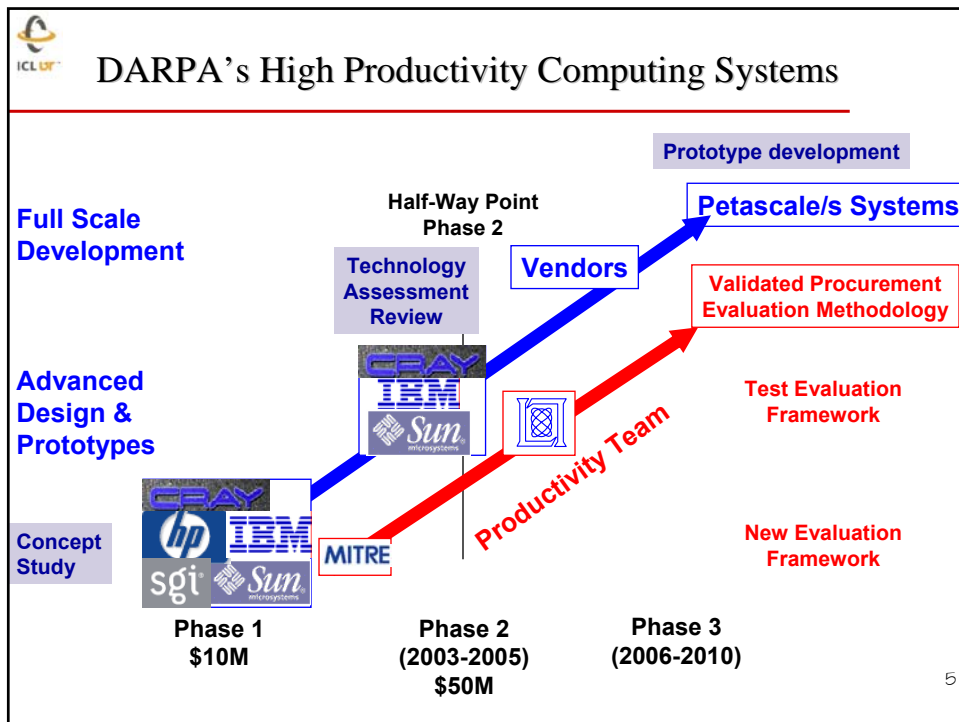


Motivation for Additional Benchmarks

Linpack Benchmark

- ♦ Good
 - One number
 - Simple to define & easy to rank
 - Allows problem size to change with machine and over time
 - Stresses the system with a long running job
- ♦ Bad
 - Emphasizes only "peak" CPU speed and number of CPUs
 - Does not stress local bandwidth
 - Does not stress the network
 - Does not test gather/scatter
 - Ignores Amdahl's Law (Only does weak scaling)
- ♦ Ugly
 - Benchmarkteering hype
- ♦ Perhaps there was a time when this was adequate.
- ♦ From Linpack Benchmark and Top500: "no single number can reflect overall performance"
- ♦ Clearly need something more than Linpack
- ♦ HPC Challenge Benchmark
 - Test suite stresses not only the processors, but the memory system and the interconnect.
 - The real utility of the HPCC benchmarks are that architectures can be described with a wider range of metrics than just Flop/s from Linpack.

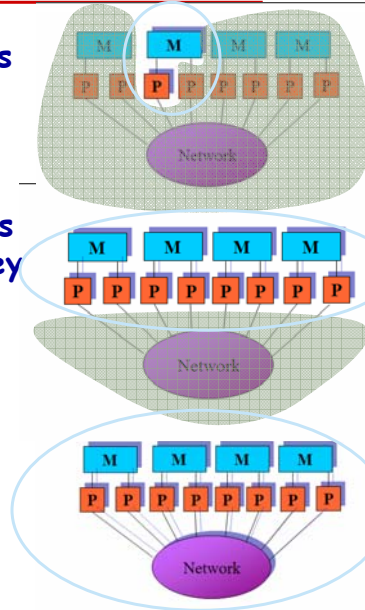
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Tests on Single Processor and System

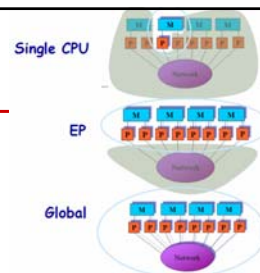
- ♦ Local - only a single processor is performing computations.
- ♦ Embarrassingly Parallel - each processor in the entire system is performing computations but they do not communicate with each other explicitly.
- ♦ Global - all processors in the system are performing computations and they explicitly communicate with each other.



HPC Challenge Benchmark

Consists of basically 7 benchmarks;
 > Think of it as a framework or harness for adding benchmarks of interest.

1. HPL (LINPACK) — MPI Global ($Ax = b$)
2. STREAM — Local; single CPU
 *STREAM — Embarrassingly parallel
3. PTRANS ($A \leftarrow A + B^T$) — MPI Global
4. RandomAccess — Local; single CPU
 *RandomAccess — Embarrassingly parallel
 RandomAccess — MPI Global
5. BW and Latency - MPI
6. FFT - Global, single CPU, and EP
7. Matrix Multiply - single CPU and EP



name	formula	bytes/sec	floats/sec
COMPL	$m(2) = m(1)$	32	8
SCALE	$m(2) = q * m(1)$	128	1
ADDE	$m(2) = m(1) + m(1)$	96	1
TRADD	$m(2) = m(1) + q * m(1)$	32	2

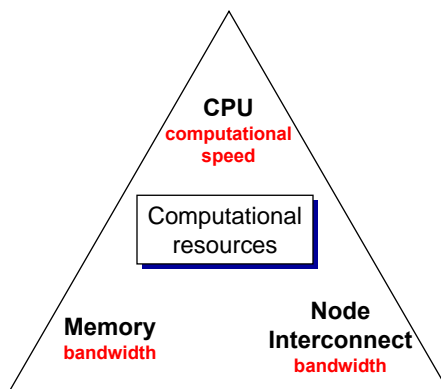
Random integer read; update; & write



HPC



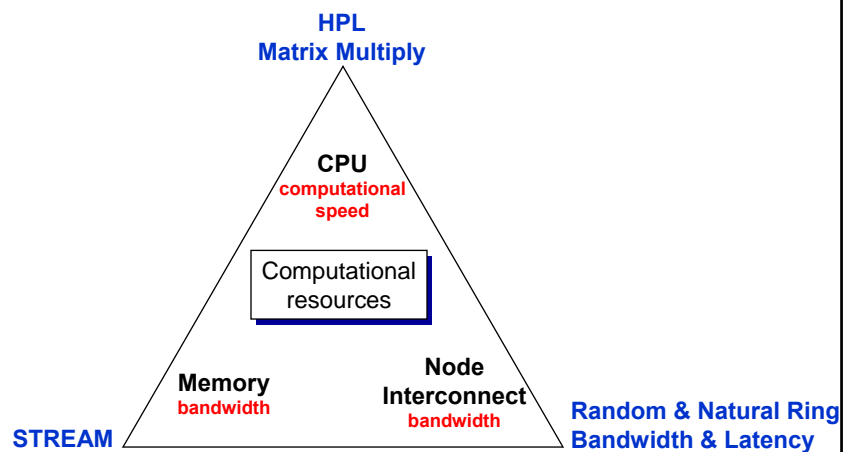
Computational Resources and HPC Challenge Benchmarks



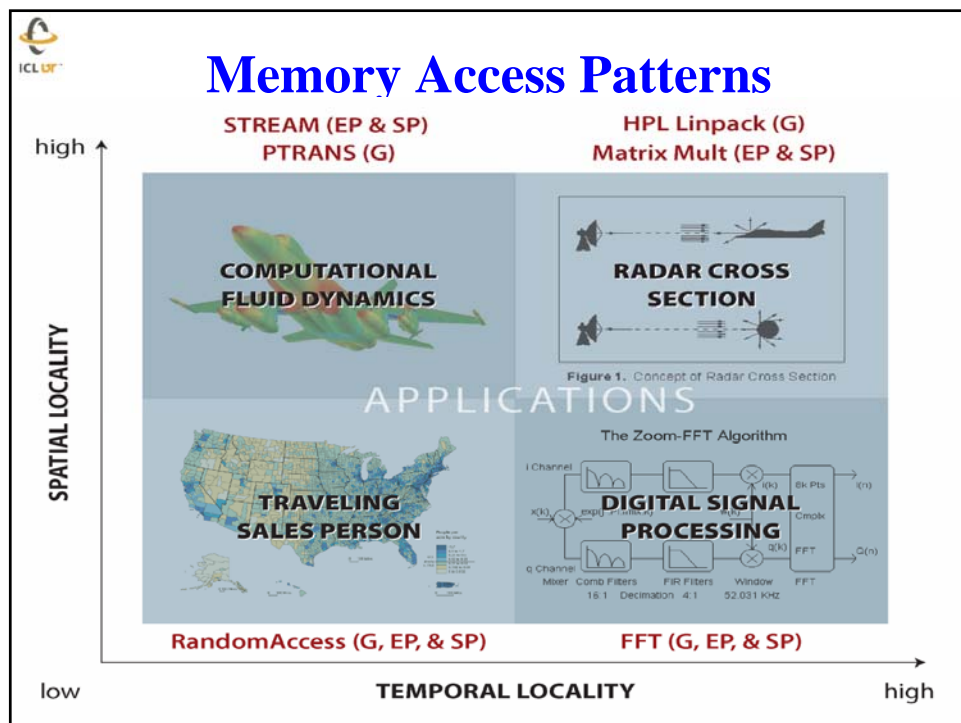
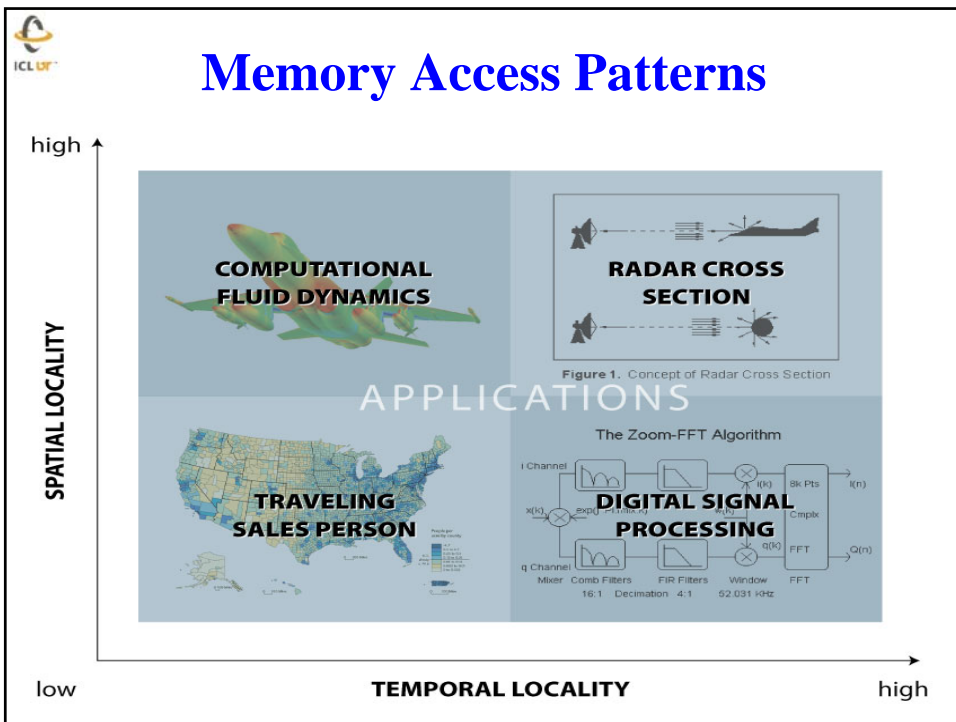
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Computational Resources and HPC Challenge Benchmarks



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How Does The Benchmarking Work?

- ♦ **Single program to download and run**
 - Simple input file similar to HPL input
- ♦ **Base Run and Optimization Run**
 - Base run must be made
 - User supplies MPI and the BLAS
 - Optimized run allowed to replace certain routines
 - User specifies what was done
- ♦ **Results upload via website**
- ♦ **html table and Excel spreadsheet generated with performance results**
 - Intentionally we are not providing a single figure of merit (no over all ranking)
- ♦ **Goal: no more than 2 X the time to execute HPL.**

HPCs



HPC Challenge Languages


	Language	HPL	Random Access	STREAM	FFT
Base	Specification	UTK	UTK	UTK	UTK
	C	UTK	UTK	UTK	UTK
	C & MPI	UTK	UTK	UTK	UTK
	C & OpenMP	UTK	UTK	UTK	UTK
	UPC		ISI		
	C & pthreads				
	C++				
	Fortran				
	Matlab	MIT-LL	MIT-LL	MIT-LL	MIT-LL
	Matlab & MPI				
	StarP	UCSB	UCSB	UCSB	UCSB
	pMatlab	MIT-LL	MIT-LL	MIT-LL	MIT-LL
	Octave	OSC	OSC	OSC	OSC
	Python				
	Python & MPI		UTK	UTK	
	Java				
	Chapel	Cray	Cray	Cray	Cray
	X10		IBM		
	Fortress				

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<http://icl.cs.utk.edu/hpcc/> [web](#)

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HPC Challenge Benchmark

The HPC Challenge benchmark consists of basically 7 benchmarks:

1. [HPL](#) - the Linpack TPP benchmark which measures the floating point rate of execution for solving a linear system of equations.
2. [DGEMM](#) - measures the floating point rate of execution of double precision real matrix-matrix multiplication.
3. [STREAM](#) - a simple synthetic benchmark program that measures sustainable memory bandwidth (in GB/s) and the corresponding computation rate for simple vector kernel.
4. [PTRANS](#) (parallel matrix transpose) - exercises the communications where pairs of processors communicate with each other simultaneously. It is a useful test of the total communications capacity of the network.
5. [RandomAccess](#) - measures the rate of integer random updates of memory (GUPS).
6. [FFTE](#) - measures the floating point rate of execution of double precision complex one-dimensional Discrete Fourier Transform (DFT).
7. Communication bandwidth and latency - a set of tests to measure latency and bandwidth of a number of simultaneous communication patterns; based on [b_eff](#) (effective bandwidth benchmark).

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HPCCHALLENGE

HPC's


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Condensed Results - Base and Optimized Runs - 76 Systems - Generated on Tue Sep 20 03:47:25 2005


System Information					Run	G-HPL	G-PTRANS	G-Random Access	G-FFTE	G-STREAM	EP-STREAM	EP-DGEMM	Randomizing Bandwidth	Randomizing Latency
System - Processor - Speed - Count - Threads - Processes					Type	TFlop/s	GB/s	Gops/s	GFlop/s	GB/s	GB/s	GFlop/s	GB/s	micro
MA/PT/PS/PC/DN/PR/CR/CS/IC/IA/SD														
Alpha Conquest cluster AMD Opteron	1.40Hz	128	1	128	base	0.2526	3.247			208.5	1.629		0.03627	23.68
ClusterVision BV Beasite AMD Opteron	2.40Hz	32	1	32	base	0.1038	0.816	0.000235	2.15	107.0	3.342	4.195	0.02648	59.29
Cray X1 MSP	0.80Hz	64	1	64	base	0.0216	3.229			939.3	14.990		0.94074	20.34
Cray X1 MSP	0.80Hz	60	1	60	base	0.0778	30.431			896.4	14.974		1.03291	20.83
Cray X1 MSP	0.80Hz	120	1	120	base	1.0610	2.460			1019.3	8.498		0.83014	20.12
Cray T3E Alpha 21164	0.60Hz	1024	1	1024	base	0.0482	10.377			928.2	0.517		0.09174	12.09
Cray X1 MSP	0.80Hz	232	1	232	base	2.2647	97.408			3759.4	14.914		0.42099	22.27
Cray X1 MSP	0.80Hz	292	1	292	opt	2.2670	96.127			5470.7	21.741		0.43020	22.64
Cray X1 MSP	0.80Hz	60	1	60	opt	0.3789	31.072			1306.1	21.768		1.00986	21.16
Cray X1 MSP	0.80Hz	124	1	124	base	1.2054	39.535			1856.7	14.973		0.70857	20.15
Cray X1 MSP	0.80Hz	124	1	124	opt	1.1820	39.382			2697.3	21.752		0.90280	20.82
PowerPC 440	1.80Hz	192	1	192	opt	1.1830	58.583			5607.5	21.762		0.80588	20.84
Cray X1 MSP	0.80Hz	60	1	60	base	0.9087	1.634	0.003075	3.14	894.1	14.902	10.915	1.16779	14.64
Cray T3E Alpha 21164	0.6750Hz	512	1	512	base	0.2232	9.774	0.028946	15.48	272.2	0.532	0.661	0.03871	8.14
Cray XE1 AMD Opteron	3.20Hz	64	1	64	base	0.2239	10.592	0.022397	16.26	170.0	2.626	4.034	0.22697	1.62
Cray X1 MSP	0.80Hz	32	1	32	base	0.2767	32.661	0.001662	2.96	475.8	14.870	8.238	1.41289	14.94
System Information					Run	G-HPL	G-PTRANS	G-Random Access	G-FFTE	G-STREAM	EP-STREAM	EP-DGEMM	Randomizing Bandwidth	Randomizing Latency
System - Processor - Speed - Count - Threads - Processes					Type	TFlop/s	GB/s	Gops/s	GFlop/s	GB/s	GB/s	GFlop/s	GB/s	micro
MA/PT/PS/PC/DN/PR/CR/CS/IC/IA/SD														
Cray XT3 AMD Opteron	2.60Hz	1100	1	1100	base	4.7823	217.923	0.137002	266.66	5274.7	4.795	4.811	0.28638	25.94
Cray mfg8 X1E	1.130Hz	240	1	240	opt	3.3889	66.010	1.854730	-1.05	3280.9	13.224	13.544	0.29866	14.58
Cray XD1 AMD Opteron	2.40Hz	128	1	128	base	0.5021	13.515	0.066672	35.52	900.1	3.307	4.334	0.25919	2.06
Cray X1E X1E MSP	1.130Hz	292	1	292	base	3.1941	85.204	0.014868	15.54	2440.0	9.682	14.185	0.26024	14.92
Cray XT3 AMD Opteron	2.40Hz	3744	1	3744	base	14.7040	608.506	0.220295	417.17	18146.4	4.847	4.413	0.16184	25.32
Cray XT3 AMD Opteron	2.40Hz	5200	1	5200	base	20.5270	874.889	0.268585	644.73	26020.8	5.004	4.395	0.14682	25.80
Cray x12 AMD Opteron	2.40Hz	32	1	32	base	0.1260	7.276	0.060602	9.27	156.4	4.808	4.776	0.27281	0.74
Cray X1E	1.130Hz	92	4	92	base	0.2376	18.920	0.008969	5.20	307.6	9.611	11.606	1.40487	12.21
Cray XT2 AMD Opteron	2.60Hz	4096	1	4096	base	16.9752	302.979	0.333072	905.57	20654.3	5.043	4.782	0.16894	9.44
Dalco Opteron/Qtlet Linux Cluster AMD Opteron	2.20Hz	64	1	64	base	0.2185	6.320	0.004705	15.55	193.4	2.397	3.876	0.17003	11.46
Dell PowerEdge 1850 cluster Intel Xeon EM64T	3.40Hz	64	1	64	base	0.2409	1.692	0.004296	10.39	81.0	1.266	5.061	0.14386	9.91
Dell PowerEdge 2650 Cluster Intel Xeon	3.45Hz	32	1	32	base	0.0966	0.910	0.000276	1.94	18.5	0.579	3.818	0.03780	42.22
Dell PowerEdge 2820 Cluster Intel Xeon	2.40Hz	32	1	32	base	0.1003	1.145	0.002339	2.20	16.7	0.983	3.782	0.04771	9.91

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Condensed Results - Base and Optimized Runs - 76 Systems - Generated on Tue Sep 20 03:45:50 2005													
System - Processor - Speed - Count - Threads - Processes	Run	G-HPL	G-PTNRANS	G-Random Access	G-FFTE	G-STREAM Total	EP-STREAM Total	EP-DGEMM	RandomRing Bandwidth	RandomRing Latency			
MA/PI/PS/PC/TH/PR/CH/CS/IC/1A/SD	Type	TFlop/s	GB/s	Gup/s	GFlop/s	GB/s	GB/s	GFlop/s	GB/s	usec			
Cray XT3 AMD Opteron	2.4GHz 5200 1 5200 base	20.5270	874.899	0.268583	644.73	26020.8	5.004	4.395	0.14682	25.80			
Cray XT3 AMD Opteron	2.4GHz 4096 1 4096 base	16.9752	302.979	0.533072	905.57	20656.5	5.043	4.782	0.16896	9.44			
Cray XT3 AMD Opteron	2.4GHz 3744 1 3744 base	14.7040	606.506	0.220236	417.17	18146.4	4.847	4.413	0.16164	25.32			
NEC SX-6	2GHz 576 1 576 base	8.0086	312.707	0.019362	160.95	23555.8	40.895	15.223	0.82924	22.27			
SGI Altix 3700 Ex2 Intel Itanium 2	1.6GHz 1008 1 1008 base	5.1383	105.666	0.032938	15.66	1907.5	1.892	5.884	0.20288	6.82			
Cray XT3 AMD Opteron	2.4GHz 1100 1 1100 base	4.7823	217.922	0.127002	266.66	5274.7	4.795	4.611	0.28620	25.94			
Cray mfg8 X1E	1.13GHz 248 1 248 opt	2.2899	66.010	1.854720	-1.00	3280.9	12.329	12.564	0.29806	14.58			
Cray X1E X1E MSP	1.13GHz 232 1 232 base	2.1941	85.204	0.014968	15.54	2440.0	9.662	14.185	0.26024	14.92			
Cray X1 MSP	0.8GHz 232 1 232 base	2.3647	97.408			3728.4	14.914		0.42099	22.27			
Cray X1 MSP	0.8GHz 232 1 232 opt	2.3670	96.127			3478.7	21.741		0.42028	22.64			
IBM Blue Gene/L PowerPC 440	0.7GHz 1024 1 1024 opt	1.4201	27.994	0.124729	49.92	862.9	0.842	2.487	0.02433	4.82			
IBM Blue Gene PowerPC 440	0.7GHz 2048 1 2048 base	1.4073	54.351	0.454032	50.15	1404.0	0.723	0.505	0.02605	4.50			
NEC SX-6	0.5GHz 192 1 192 base	1.3271	92.968	0.006030	36.13	8051.3	26.508	7.941	0.39777	25.67			
Cray X1 MSP	0.8GHz 124 1 124 base	1.2054	59.525			1856.7	14.975		0.70887	20.18			
Cray X1 MSP	0.8GHz 124 1 124 opt	1.1820	59.563			2697.3	21.752		0.80366	20.85			
Cray X1 MSP	0.8GHz 124 1 124 opt	1.1820	59.563			2697.3	21.752		0.80366	20.85			
System - Processor - Speed - Count - Threads - Processes	Run	G-HPL	G-PTNRANS	G-Random Access	G-FFTE	G-STREAM Total	EP-STREAM Total	EP-DGEMM	RandomRing Bandwidth	RandomRing Latency			
MA/PI/PS/PC/TH/PR/CH/CS/IC/1A/SD	Type	TFlop/s	GB/s	Gup/s	GFlop/s	GB/s	GB/s	GFlop/s	GB/s	usec			
IBM eServer pSeries 625 Power 4+	1.7GHz 256 4 64 base	1.0744	23.721	0.005902	10.46	411.7	6.433	17.979	0.72395	8.34			
Cray X1 MSP	0.8GHz 120 1 120 base	1.0410	2.460			1019.5	8.496		0.83014	20.12			
Linux Netopix Power Intel Xeon	2.06GHz 256 1 256 base	1.0303	3.113			198.2	0.774		0.03266	22.27			
NEC SX-6	0.5GHz 128 1 128 base	0.9049	61.402	0.004246	37.16	3437.9	26.858	7.938	0.42824	27.43			
IBM p690 Power 4	1.2GHz 204 1 204 base	0.9030	5.002			864.3	1.715		0.01036	367.48			
IBM Blue Gene PowerPC 440	0.7GHz 1024 1 1024 opt	0.7901	26.440	0.299617	70.94	765.3	0.747	0.901	0.04480	4.50			
IBM Blue Gene PowerPC 440	0.7GHz 1024 1 1024 base	0.7164	27.578	0.134954	48.99	868.4	0.848	0.919	0.03461	4.81			
IBM p690 Power 4	1.2GHz 256 1 256 base	0.6597	0.835			304.4	1.190		0.00456	375.99			
SGI Altix 3700 Intel Itanium 2	1.5GHz 128 1 128 base	0.6389	7.923	0.011243	14.09	276.3	2.158	5.818	0.21069	6.39			
HP AlphaServer SC45 Alpha 21264B	1GHz 484 1 484 base	0.6181	3.739			672.5	1.389		0.02269	39.91			
HP Compaq SC45 Alpha 21264C	1GHz 484 1 484 base	0.5805	6.370	0.008090	5.01	630.9	1.303	1.218	0.02260	39.63			
Cray X1 MSP	0.8GHz 60 1 60 opt	0.5789	31.073			1506.1	21.748		1.00886	21.16			

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The values plotted for HPL, PTNRANS, RandomAccess, and FFT are per processor. The values plotted for SN-DGEMM and SN-STREAM are per thread. The value plotted for RandomRing Latency is normalized using it's reciprocal. Only those systems that have values for all the tests plotted are available for the diagram. Use the left-hand column to select up to 6 systems to plot in the Kiviat diagram.

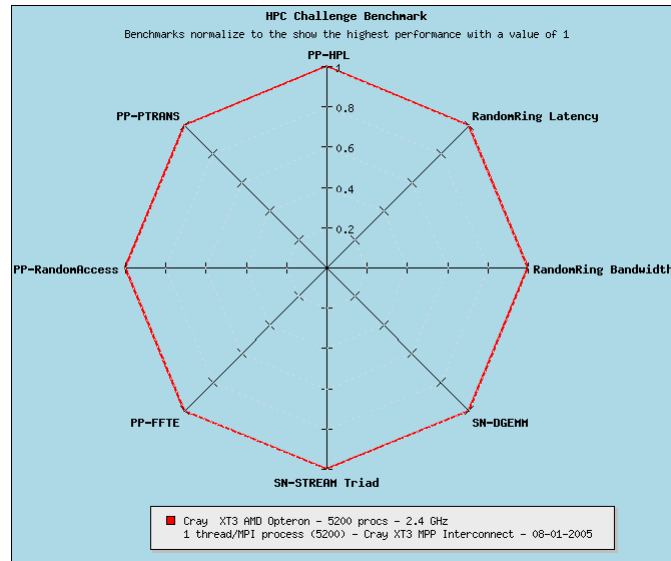
Systems for Kiviat Chart - Base Runs Only - 47 Systems - Generated on Mon Sep 26 08:12:42 2005													
Plot	System Information System - Processor - Speed - Count - Threads - Processes				PP-HPL	PP-PTNRANS	PP-Random Access	PP-SN-STREAM Total	PP-FFTE	PP-SN-DGEMM	RandomRing Bandwidth	RandomRing Latency	
	MA/PI/PS/PC/TH/PR/CH/CS/IC/1A/SD				TFlop/s	GB/s	Gup/s	GB/s	GFlop/s	GFlop/s	GB/s	usec	
<input type="checkbox"/>	ClusterVision BV Beasite AMD Opteron				2.4GHz 32 1 32	0.002242	0.023490	(0.00000724)	2.329	(0.06709)	4.200	0.0265	32.22
<input type="checkbox"/>	Cray X1 MSP				0.8GHz 60 1 60	0.008479	0.027237	(0.00005125)	16.211	(0.05241)	10.904	1.1678	14.66
<input type="checkbox"/>	Cray T3E Alpha 21164				0.675GHz 512 1 512	0.000436	0.019090	(0.00005654)	0.542	(0.03023)	0.680	0.0357	6.14
<input type="checkbox"/>	Cray XD1 AMD Opteron				2.2GHz 64 1 64	0.003498	0.165506	(0.00034995)	2.766	(0.25564)	3.980	0.2275	1.63
<input type="checkbox"/>	Cray X1 MSP				0.8GHz 32 1 32	0.008647	1.020644	(0.00005194)	16.221	(0.09265)	9.459	1.4127	14.94
<input type="checkbox"/>	Cray XT3 AMD Opteron				2.6GHz 1100 1 1100	0.004340	0.198112	(0.00012455)	4.989	(0.24242)	4.811	0.2864	25.94
<input type="checkbox"/>	Cray XD1 AMD Opteron				2.4GHz 128 1 128	0.003922	0.105590	(0.00052088)	4.358	(0.27748)	4.334	0.2592	2.06
<input type="checkbox"/>	Cray X1E X1E MSP				1.13GHz 232 1 232	0.012675	0.338111	(0.00003900)	23.129	(0.06165)	15.156	0.3602	14.93
<input type="checkbox"/>	Cray XT3 AMD Opteron				2.4GHz 3744 1 3744	0.003927	0.162328	(0.00003884)	4.621	(0.11142)	4.414	0.1616	25.32
<input type="checkbox"/>	Cray XT3 AMD Opteron				2.4GHz 5200 1 5200	0.003947	0.168290	(0.00005163)	4.720	(0.12399)	4.393	0.1468	25.80
<input type="checkbox"/>	Cray x3 AMD Opteron				2.4GHz 32 1 32	0.004337	0.230513	(0.00018938)	4.888	(0.29276)	4.773	0.3728	8.74
<input type="checkbox"/>	Cray X1E				1.13GHz 32 4 32	0.010581	0.591247	(0.00028027)	9.710	(0.16258)	3.629	1.4049	12.21
<input type="checkbox"/>	Cray XT3 AMD Opteron				2.6GHz 4096 1 4096	0.004144	0.073969	(0.00013014)	5.042	(0.22109)	4.775	0.1690	9.44
<input type="checkbox"/>	Balto Opteron/Quintel Linux Cluster AMD Opteron				2.2GHz 64 1 64	0.003407	0.088742	(0.00007344)	2.432	(0.21149)	3.893	0.1700	11.46
<input type="checkbox"/>	Dell PowerEdge 2850 Cluster Intel Xeon EM64T				3.4GHz 64 1 64	0.005481	0.029568	(0.00006690)	2.844	(0.16232)	6.152	0.1459	9.81
<input type="checkbox"/>	Dell PowerEdge 2650 Cluster Intel Xeon				2.4GHz 32 1 32	0.003020	0.028422	(0.00008842)	1.567	(0.06053)	3.999	0.0379	42.23
Plot	System Information System - Processor - Speed - Count - Threads - Processes				PP-HPL	PP-PTNRANS	PP-Random Access	PP-SN-STREAM Total	PP-FFTE	PP-SN-DGEMM	RandomRing Bandwidth	RandomRing Latency	
	MA/PI/PS/PC/TH/PR/CH/CS/IC/1A/SD				TFlop/s	GB/s	Gup/s	GB/s	GFlop/s	GFlop/s	GB/s	usec	
<input type="checkbox"/>	Dell PowerEdge 2650 Cluster Intel Xeon				2.4GHz 32 1 32	0.003130	0.035796	(0.00006994)	1.197	(0.06863)	4.025	0.0477	8.91
<input type="checkbox"/>	Dell PowerEdge 2650 Cluster Intel Xeon				2.4GHz 32 1 32	0.003031	0.043274	(0.00006383)	1.182	(0.07333)	4.039	0.0659	19.00
<input type="checkbox"/>	Dell PowerEdge 2650 Cluster Intel Xeon				2.4GHz 32 1 32	0.003169	0.063993	(0.00005507)	1.147	(0.10128)	3.999	0.1784	9.88
<input type="checkbox"/>	HP Compaq SC45 Alpha 21264C				1GHz 484 1 484	0.001199	0.013162	(0.00001671)	1.642	(0.01035)	1.748	0.0226	39.63
<input type="checkbox"/>	HP SC-40 Alpha 21264B				0.833GHz 484 1 484	0.000996	0.010290	(0.00001299)	1.219	(0.00932)	1.449	0.0173	50.10
<input type="checkbox"/>	IBM eServer pSeries 435 Power 4+				1.7GHz 256 4 64	0.004197	0.092661	(0.00002149)	2.912	(0.04087)	4.706	0.7240	8.34
<input type="checkbox"/>	IBM eServer pSeries 635 Power 4+				1.7GHz 128 4 32	0.004154	0.060726	(0.00002211)	3.009	(0.03424)	4.733	0.7472	7.94
<input type="checkbox"/>	IBM eServer pSeries 635 Power 4+				1.7GHz 64 4 16	0.004085	0.063680	(0.00002893)	2.867	(0.03058)	4.726	0.7483	7.93

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HPCC Kiviat Chart



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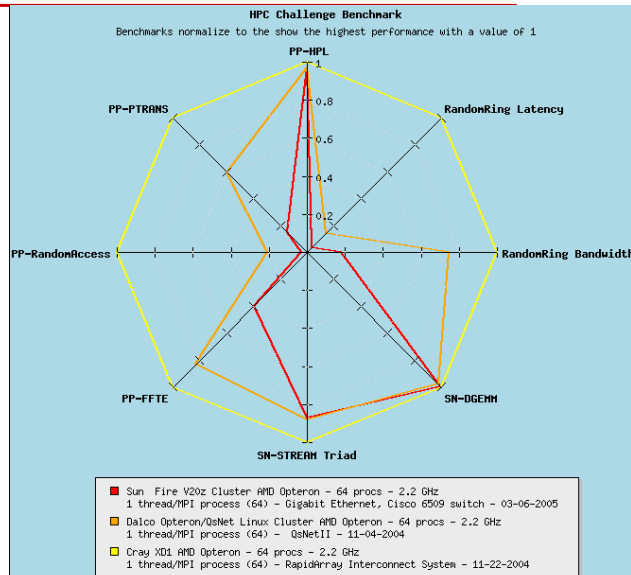


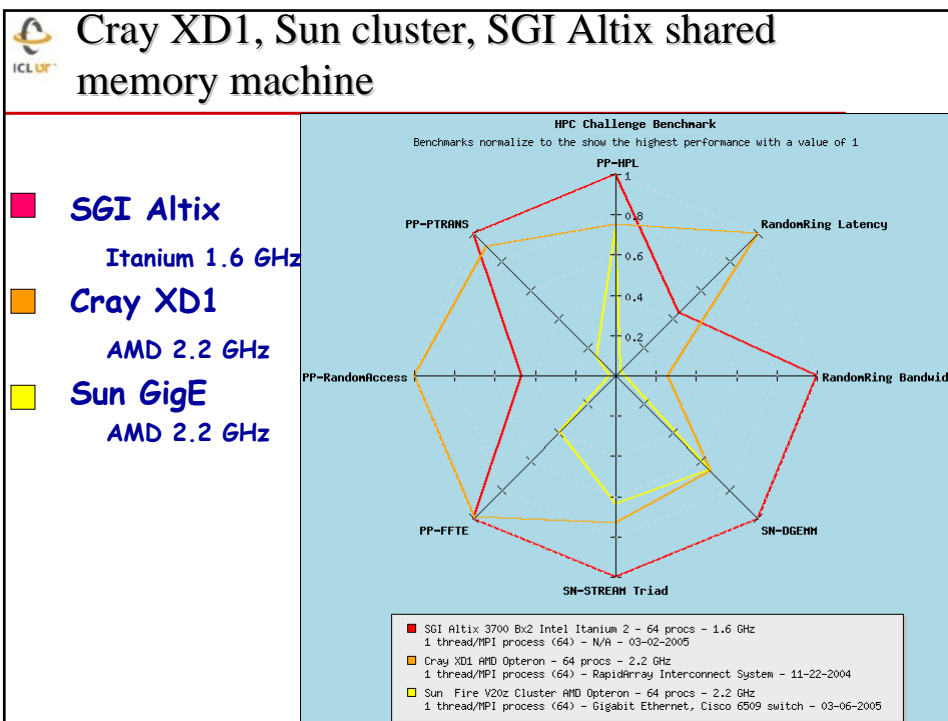
HPCC: A Comparison of 3 Systems

- ♦ Three systems using the same processor and number of processors.

- AMD Opteron 64 processors 2.2 GHz

- Cray XD1
Custom Interconnect
- Dalco Linux Cluster
Quadrics Interconnect
- Sun Fire Cluster
Gigabit ethernet Interconnect





HPCC Awards 2005 Info and Rules

ICLUT

Class 1 (Objective)

- ♦ **Performance**
 1. **G-HPL \$500**
 2. **G-RandomAccess \$500**
 3. **EP-STREAM system \$500**
 4. **G-FFT \$500**
- ♦ **Only full submissions from HPCC database**

Sponsored by:

Class 2 (Subjective)

- ♦ **Productivity (Elegant Implementation)**
 - **Implement at least two tests from Class 1**
 - **\$1500 (may be split)**
 - **Deadline:**
 - **October 15, 2005**
 - **Select 3 as finalists**
- ♦ **Submissions format flexible**
 - **Finalists will present at SC|05 in the HPCC BOF**

Winners (in both classes) will be announced at SC|05 HPCC BOF on Tuesday November 15th at noon.

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Class 1:

If Awards Given Today, the Winners ...

- ♦ **Global HPL**
 - Cray XT3 AMD Opteron ORNL
 - 5200 proc; 2.4 GHz Opteron
 - 20.5 Tflop/s
- ♦ **Global Random Access**
 - Cray X1E ORNL
 - 248 proc; Cray X1E 1.13 GHz
 - Optimized run using UPC.
 - 1.855 Gup/s
- ♦ **EP-Stream (triad) for the System**
 - Cray XT3 AMD Opteron ORNL
 - 5200 proc; 2.4 GHz Opteron
 - 26020 GB/s
- ♦ **Global FFT**
 - Cray XT3 AMD Opteron DOD-ERDC
 - 4096 proc; 2.6 GHz Opteron
 - 906 Gflop/s

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Class 2: Implementation Languages (Subjective)

- | | |
|---|---|
| ♦ English (Paper and pencil) | |
| ♦ C/C++ <ul style="list-style-type: none">➢ MPI-1, MPI-2, OpenMP, pthreads | ♦ HPCC tests <ul style="list-style-type: none">➢ FFT➢ HPL➢ RandomAccess➢ STREAM |
| ♦ Fortran 90/95/03 | |
| ♦ Java | |
| ♦ Matlab <ul style="list-style-type: none">➢ MatlabMPI, StarP, pMatlab | ♦ Good if 2 of the 4 tests actually run |
| ♦ Python <ul style="list-style-type: none">➢ MPI | |
| ♦ UPC, CAF | |
| ♦ Chapel, X10, Fortress | |

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Committee Members

- ♦ **David Bailey**
 - LBNL NERSC
- ♦ **Jack Dongarra (Co-Chair)**
 - University of Tenn/ORNL
- ♦ **Jeremy Kepner (Co-Chair)**
 - MIT Lincoln Lab
- ♦ **David Koester**
 - MITRE
- ♦ **Bob Lucas**
 - ISI
- ♦ **Rusty Lusk**
 - Argonne National Lab
- ♦ **Piotr Luszczek**
 - University of Tennessee
- ♦ **John McCalpin**
 - IBM Austin
- ♦ **Rolf Rabenseifner**
 - HLRS, Stuttgart
- ♦ **Daisuke Takahashi**
 - University of Tsukuba

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Future Directions

- ♦ Looking at reducing execution time
- ♦ Constructing a framework for benchmarks
- ♦ Developing machine signatures
- ♦ Plans are to expand the benchmark collection
 - Sparse matrix operations
 - I/O
 - Smith-Waterman (sequence alignment)
- ♦ Port to new systems
- ♦ Provide more implementations
 - Languages (Fortran, UPC, Co-Array)
 - Environments
 - Paradigms



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Collaborators

♦ HPC Challenge

- Piotr Łuszczek, U of Tennessee
- David Bailey, NERSC/LBL
- Jeremy Kepner, MIT Lincoln Lab
- David Koester, MITRE
- Bob Lucas, ISI/USC
- Rusty Lusk, ANL
- John McCalpin, IBM, Austin
- Rolf Rabenseifner, HLRS Stuttgart
- Daisuke Takahashi, Tsukuba, Japan



<http://icl.cs.utk.edu/hpcc/>



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