HPC Challenge Benchmarks: An Expanded View of High End Computers

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Panel Members
http://icl.cs.utk.edu/hpcc/

- David Koester, MITRE
  Overview of HPC Program and Performance Evaluation
- Piotr Luszczek, University of Tennessee
  HPCC Benchmark Overview and Results
- John McCaplin, IBM Austin
  Optimum System Balance for Systems of Finite Price
- Bob Lucas, ISI/USC
  Execution Time Modeling and Experiments
- Jeff Vetter, Oak Ridge National Lab
  Differences in baseline and optimized versions of HPCC
- Allan Snavely, UCSD/SDSC
  Dimensions of Spatial and Temporal locality
- Jeremy Kepner, MIT Lincoln Lab
  Synthetic Benchmark and Matlab
- David Nelson, National Coordination Office
  Government/Policy Context

Panelists have been asked to …

- Describe the HPC Challenge Framework
- Examine the architecture stresses of the benchmark
- Examine the relationships between the benchmarks and real applications performance
- Examine the political and business motivations to develop and publicize the HPCC benchmarks that look beyond Linpack and the Top500 list.

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